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ATTORNEY DOCKET NO. 02887.0259
CUSTOMER NO. 22,852



Mail Stop Patent Application

**Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450**

New U.S. Patent Application

**Title: SEMICONDUCTOR WAFER TREATMENT METHOD,
SEMICONDUCTOR WAFER INSPECTION METHOD,
SEMICONDUCTOR DEVICE DEVELOPMENT METHOD AND
SEMICONDUCTOR WAFER TREATMENT APPARATUS**

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Sir:

We enclose the following papers for filing in the United States Patent and Trademark Office in connection with the above patent application.

1. Application- 35 pages, including 5 independent claims and 31 claims total.

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2. Drawings- 10 sheets of formal drawings containing 20 figures. (Figures 1-16).
3. The filing fee is calculated as follows:

Basic Application Filing Fee					\$770	\$ \$770.00
	Number of Claims		Basic	Extra Claims		
Total Claims	31	-	20	11	x \$18	198.00
Independent Claims	5	-	3	2	x \$86	172.00
<input type="checkbox"/> Presentation of Multiple Dep. Claim(s)					+\$290	
Subtotal						\$ 1140.00
Reduction by 1/2 if small entity						-
TOTAL APPLICATION FILING FEE						\$ 1140.00

4. A check for \$1140.00 is enclosed. The fee includes:

\$770.00 filing fee; and
\$370.00 additional claims fee

This application is being filed under the provisions of 37 C.F.R. § 1.53(f). Applicants await notification from the Patent and Trademark Office of the time set for filing the Declaration.

Applicants claim the right to priority based on Japanese Application Nos. 2002-330683, filed November 14, 2002, and 2003-372019, filed October 31, 2003.

Please address all correspondence with respect to this application to:

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Please accord this application an application number and filing date.

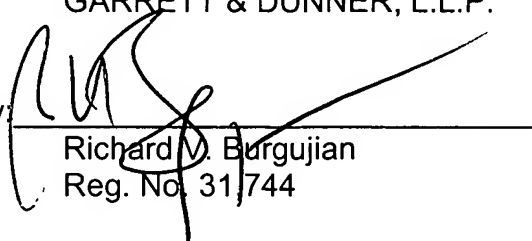
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The Commissioner is hereby authorized to charge any additional filing fees due and any other fees due under 37 C.F.R. § 1.16 or §1.17 during the pendency of this application to our Deposit Account No. 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,
GARRETT & DUNNER, L.L.P.

By



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RVB/FPD/gah
Enclosures